```
2/3 WPIL - (C) Derwent
    - 1987-258380 [37]
ΧP
    - N1987-193459
TI
    - Clock synchronising method for bus local network - has information
     transmitted as data packages from master node to nodes requiring
      synchronising and connected to common data channel
DC
    - W01 W02
AW
    - MILITARY APPLY
    - (PHIG ) PHILIPS GLOEILAMPENFAB NV
    - (PHIG ) PHILIPS NORDEN AB
IN
    - BENSON BH; EDSTROEM PA; EDSTROM PA
NP
NC
    - 7_
     EP-237106
PN
                      19870916 DW1987-37 Eng 14p *
                   Α
      AP: 1987EP-0200318 19870225
      DSR: DE FR GB
    - SE8601073
                   Α
                      19870908 DW1987-43
    - SE-452231
                   B 19871116 DW1987-48
    - JP62271540 A 19871125 DW1988-02
    - DK8701116
                   Α
                      19870908 DW1988-03
    - US4815110
                   Α
                      19890321 DW1989-14 8p
          1987US-0023411 19870309
    - EP-237106
                  B1 19930512 DW1993-19 H04J-003/06 Eng 12p
           1987EP-0200318 19870225
      DSR: DE FR GB SE
    - DE3785780
                   G
                      19930617 DW1993-25 H04J-003/06
          Based on EP-237106
           1987DE-3785780 19870225; 1987EP-0200318 19870225
    - DK-166524
                   B 19930601 DW1993-27 H04L-007/04
      FD:
           DK8701116
     AP:
           1987DK-0001116 19870304
PR
    - 1986SE-0001073 19860307
    - EP-131662; US4473889; US4596025
     A3...8935; No-SR.Pub
IC
    - H04J-003/06 H04L-007/04 G06F-001/12 H04L-011/00
    - EP-237106 A
AB
     A synchronising message is transmitted from one node which has been
     chosen to be the master to all nodes, the master node inclusive. When
     receiving a synchronising message the nodes read the clock state. Then
     the master node transmits a clock time message containing the master
     node clock state when it received the synchromising message. The clock
     state indicated by the received clock time message is compared in the
     slave nodes with the clock states which have been read in the slave
     nodes.
    - The clock states of the read clocks are compared with the received
     master clock state, and the clock states of the slave nodes, are
     corrected in dependence on the outcome of the comparison.
    - ADVANTAGE - Accurate synchronising.
MC
   - EPI: W01-A04A W02-K02A
UP
    - 1987-37
    - 1987-43; 1987-48; 1988-02; 1988-03; 1989-14; 1993-19; 1993-25; 1993-27
3/3 WPIL - (C) Derwent
AN - 1987-055524 [08]
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